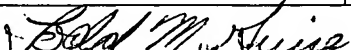


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PTO/SB/05 (03-01)

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UTILITY PATENT APPLICATION TRANSMITTAL		Attorney Docket No. GK-US035197	
(Only for new nonprovisional applications under 37 C.F.R. 1.53(b))		First Inventor	Min-Su Jeong et al.
		Title	PHASE LOCKED LOOP FREQUENCY SYNTHESIZER WHERE FREQUENCY GAIN VARIATION OF VOLTAGE CONTROLLED OSCILLATOR IS COMPENSATED
Express Mail Label No.			
APPLICATION ELEMENTS		ADDRESS TO:	
See MPEP chapter 600 concerning utility patent application contents.		Assistant Commissioner for Patents Box Patent Application Washington, DC 20231	
<div>1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g., PTO/SB/17) <i>(Submit an original and a duplicate for fee processing)</i></div> <div>2. <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27.</div> <div>3. <input checked="" type="checkbox"/> Specification [Total Pages <input type="text" value="45"/>] <i>(preferred arrangement set forth below)</i><ul style="list-style-type: none">- Descriptive title of the Invention- Cross Reference to Related Applications- Statement Regarding Fed sponsored R & D- Reference to sequence listing, a table, or a computer program listing appendix- Background of the Invention- Brief Summary of the Invention- Brief Description of the Drawings (if filed)- Detailed Description- Claim(s)- Abstract of the Disclosure</div> <div>4. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113) [Total Sheets <input type="text" value="6"/>]</div> <div>5. <input type="checkbox"/> Oath or Declaration [Total Pages <input type="text" value="2"/>]<div style="margin-left: 20px;"><div>a. <input checked="" type="checkbox"/> Newly executed (original or copy)</div><div>b. <input type="checkbox"/> Copy from a prior application (37 CFR 1.63 (d)) <i>(for a continuation/divisional with Box 18 completed)</i></div><div>i. <input type="checkbox"/> DELETION OF INVENTOR(S) Signed statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).</div></div></div> <div>6. <input type="checkbox"/> Application Data Sheet. See 37 CFR 1.76</div>		<div>7. <input type="checkbox"/> CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)</div> <div>8. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)<div style="margin-left: 20px;"><div>a. <input type="checkbox"/> Computer Readable Form (CRF)</div><div>b. Specification Sequence Listing on:<div style="margin-left: 20px;"><div>i. <input type="checkbox"/> CD-ROM or CD-R (2 copies); or</div><div>ii. <input type="checkbox"/> paper</div></div></div><div>c. <input type="checkbox"/> Statements verifying identity of above copies</div></div></div>	
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		<div>9. <input checked="" type="checkbox"/> Assignment Papers (cover sheet & document(s))</div> <div>10. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement <input type="checkbox"/> Power of Attorney <i>(when there is an assignee)</i></div> <div>11. <input type="checkbox"/> English Translation Document (if applicable)</div> <div>12. <input checked="" type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449 <input checked="" type="checkbox"/> Copies of IDS Citations</div> <div>13. <input type="checkbox"/> Preliminary Amendment</div> <div>14. <input checked="" type="checkbox"/> Return Receipt Postcard (MPEP 503) <i>(Should be specifically itemized)</i></div> <div>15. <input checked="" type="checkbox"/> Certified Copy of Priority Document(s) <i>(if foreign priority is claimed)</i></div> <div>16. <input type="checkbox"/> Nonpublication Request under 35 U.S.C. 122 (b)(2)(B)(i). Applicant must attach form PTO/SB/35 or its equivalent.</div> <div>17. <input type="checkbox"/> Other: _____</div>	
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Address	1233 Twentieth Street, NW Suite 700		
City	Washington	State	DC
		Zip Code	20036-2680
Country	United States	Telephone	(202) 293-0444
		Fax	(202) 293-0445
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Signature		Date	10/1/03

[Page 1 of 2]

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Attorney Docket No.: **GK-US035197**

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BOX: PATENT APPLICATION
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Transmitted herewith for filing is the patent application of

Inventor: **Min-Su JEONG et al.**

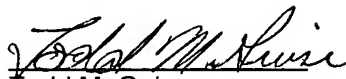
For: **PHASE LOCKED LOOP FREQUENCY SYNTHESIZER WHERE FREQUENCY GAIN
VARIATION OF VOLTAGE CONTROLLED OSCILATOR IS COMPENSATED**

The filing fee for filing this patent application has been calculated as follows:

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FOR:	NO. FILED		NO. EXTRA	RATE	FEE		RATE	FEE
BASIC FEE					\$385.00	OR		\$770.00
TOTAL	19	- 20 =	0	x 09 =	\$		x 18 =	\$
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Dated: 12/1/03


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Reg. No. 46,748

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